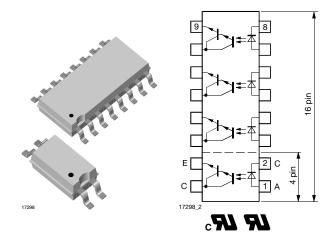


Vishay Semiconductors

Optocoupler, Photodarlington Output, High Gain, Single/Quad Channel, Half Pitch Mini-Flat Package



DESCRIPTION

The TCMD1000, TCMD4000 consist of a photodarlington optically coupled to a gallium arsenide infrared-emitting diodes in either a 4 pin or 16 pin miniflat package.

The elements provide a fixed distance between input and output for highest safety requirements.

FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}
- Low coupling capacitance of typical 0.3 pF
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





COMPLIANT

GREEN

(5-2008)

APPICLATIONS

- Programmable logic
- Modems
- Answering machines
- General applications

AGENCY APPROVALS

- UL1577, file no. E76222 system code M, double protection
- CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO
- BSI

ORDERING	INFORM	IATION							
Т	С	М	D	#	0	0	0	SOP-#	
			PART N	UMBER				7 mm ▶	
AGENCY CER	AGENCY CERTIFIED/PACKAGE				CTR (%)				
UL, cUL, FIMKO, BSI, VDE				> 600					
SOP-4					TCMD1000				
SOP-16, quad	channel					TCMD4000			

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER TEST CONDITION SYMBOL VALUE UNIT									
INPUT									
Reverse voltage		V _R	6	V					
Forward current		I _F	60	mA					
Forward surge current	t _P ≤ 10 μs	I _{FSM}	1.5	Α					
Power dissipation		P _{diss}	100	mW					
Junction temperature		Tj	125	°C					



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ABSOLUTE MAXIMUM RATI	NGS (T _{amb} = 25 °C, unle	ss otherwise spec	ified)	
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
OUTPUT				
Collector emitter voltage		V _{CEO}	35	V
Emitter collector voltage		V _{ECO}	7	V
Collector current		I _C	80	mA
Collector peak current	$t_P/T = 0.5, t_P \le 10 \text{ ms}$	I _{CM}	100	mA
Power dissipation		P _{diss}	150	mW
Junction temperature		Tj	125	°C
COUPLER				
AC isolation test voltage (RMS)		V _{ISO} (1)	3750	V _{RMS}
Total power dissipation		P _{tot}	250	mW
Operating ambient temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 125	°C
Soldering temperature (2)		T _{sld}	260	°C

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Related to standard climate 23/50 DIN 50014.
- (2) Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (www.vishav.com/doc?80054).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
input	input							
Forward voltage	$I_F = 50 \text{ mA}$	V _F		1.25	1.6	V		
Junction capacitance	V _R = 0 V, f = 1 MHz	C _j		50		pF		
output	output							
Collector emitter voltage	I _C = 100 μA	V_{CEO}	35			V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V		
Collector dark current	$V_{CE} = 10 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA		
coupler								
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 5 \text{ mA}$	V _{CEsat}			1	V		
Cut-off frequency	$I_F = 10 \text{ mA, } V_{CE} = 5 \text{ V,}$ $R_L = 100 \ \Omega$	f _c		10		kHz		
Coupling capacitance	f = 1 MHz	C _k		0.3		pF		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION PART SYMBOL MIN. TYP. MAX. UN								
I _C /I _E	V _{CE} = 2 V, I _F = 1 mA TCMD1000 CTR 600 800 TCMD4000 CTR 600 800	800		%					
I C/ IF		TCMD4000	CTR	600	800		%		

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Rise time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see figure 1)	t _r		300		μs		
Turn-off time	V_{CE} = 2 V, I_{C} = 10 mA, R_{L} = 100 Ω (see figure 1)	t _{off}		250		μs		

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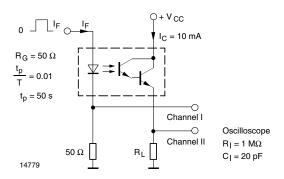


Fig. 1 - Test Circuit, Non-Saturated Operation

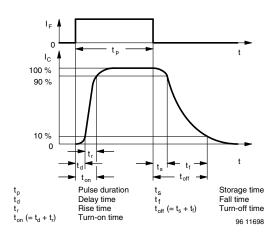


Fig. 2 - Switching Times

SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification	IEC 68 part 1			40/110/21			
Comparative tracking index		CTI	175		399		
V _{IOTM}			6000			V	
V_{IORM}			707			V	
P _{SO}					265	mW	
I _{SI}					130	mA	
T _{SI}					150	°C	
Creepage distance			5			mm	
Clearance distance			5			mm	
Insulation thickness, reinforce rated	per IEC 60950 2.10.5.1		0.4			mm	

Note

As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

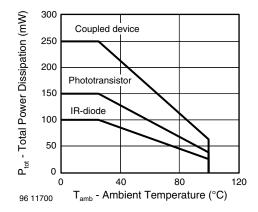


Fig. 3 - Forward Voltage vs. Ambient Temperature

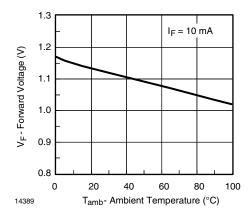


Fig. 4 - Forward Voltage vs. Ambient Temperature

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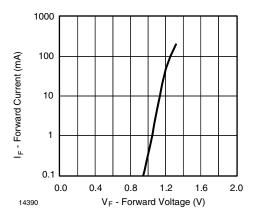


Fig. 5 - Forward Current vs. Forward Voltage

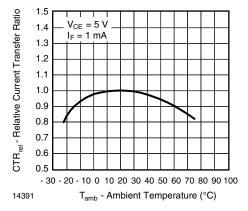


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

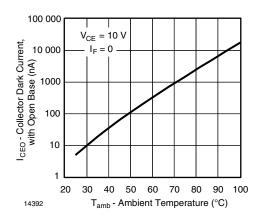


Fig. 7 - Collector Dark Current vs. Ambient Temperature

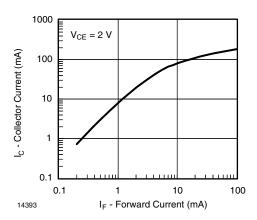


Fig. 8 - Collector Current vs. Forward Current

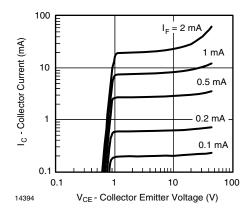


Fig. 9 - Collector Current vs. Collector Emitter Voltage

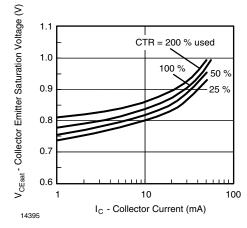


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current



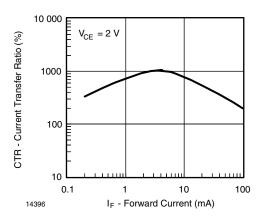
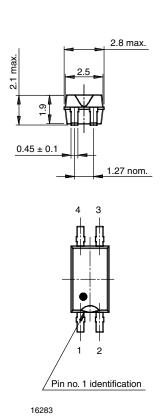
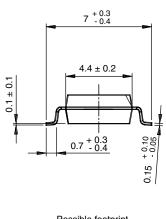
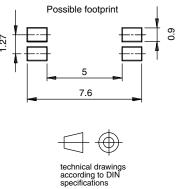


Fig. 11 - Current Transfer Ratio vs. Forward Current

PACKAGE DIMENSIONS in millimeters



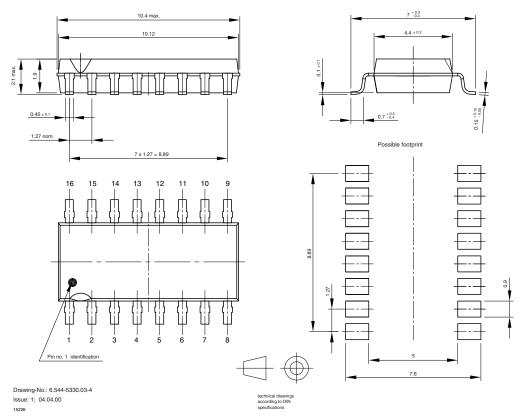






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PACKAGE MARKING





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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

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